



US 20190206321A1

(19) **United States**

(12) **Patent Application Publication**
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(10) **Pub. No.: US 2019/0206321 A1**
(43) **Pub. Date: Jul. 4, 2019**

(54) **ORGANIC LIGHT-EMITTING DIODE
DISPLAY WITH EXTERNAL
COMPENSATION**

Publication Classification

(51) **Int. Cl.**
G09G 3/3233 (2006.01)
G09G 3/3291 (2006.01)
H01L 27/32 (2006.01)
(52) **U.S. Cl.**
CPC *G09G 3/3233* (2013.01); *G09G 3/3291*
(2013.01); *H01L 27/3276* (2013.01); *G09G*
2320/0693 (2013.01); *G09G 2300/0426*
(2013.01); *G09G 2300/0439* (2013.01); *G09G*
2300/0819 (2013.01)

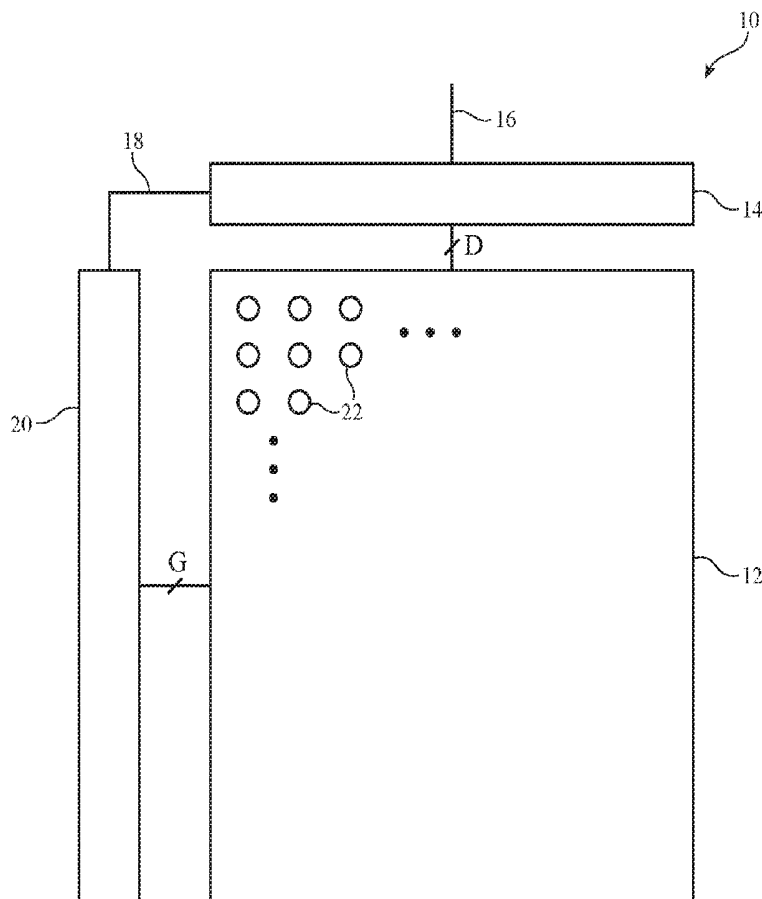
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(21) Appl. No.: **16/325,236**
(22) PCT Filed: **Aug. 10, 2017**
(86) PCT No.: **PCT/US2017/046356**
§ 371 (c)(1),
(2) Date: **Feb. 13, 2019**

(57) **ABSTRACT**
A display has rows and columns of pixels (22). A data line (Dn) in each column provides image data signals to the pixels of that column. Each row has first and second control lines (select[m], monitor[m]) coupled to the gates of first and second respective transistors (SE, MO) in each pixel. A third transistor in each pixel serves as a drive transistor (DR) and is coupled in series with a light-emitting diode (30) between positive and ground power supply voltages (VDD, VSS). A display driver circuitry in the display characterizes each of the light-emitting diodes in a column using the data line in an adjacent column from that light-emitting diode. Each of the drive transistors in a column is characterized using the data line in that column and the data line in an adjacent column.

Related U.S. Application Data

(60) Provisional application No. 62/375,650, filed on Aug. 16, 2016.



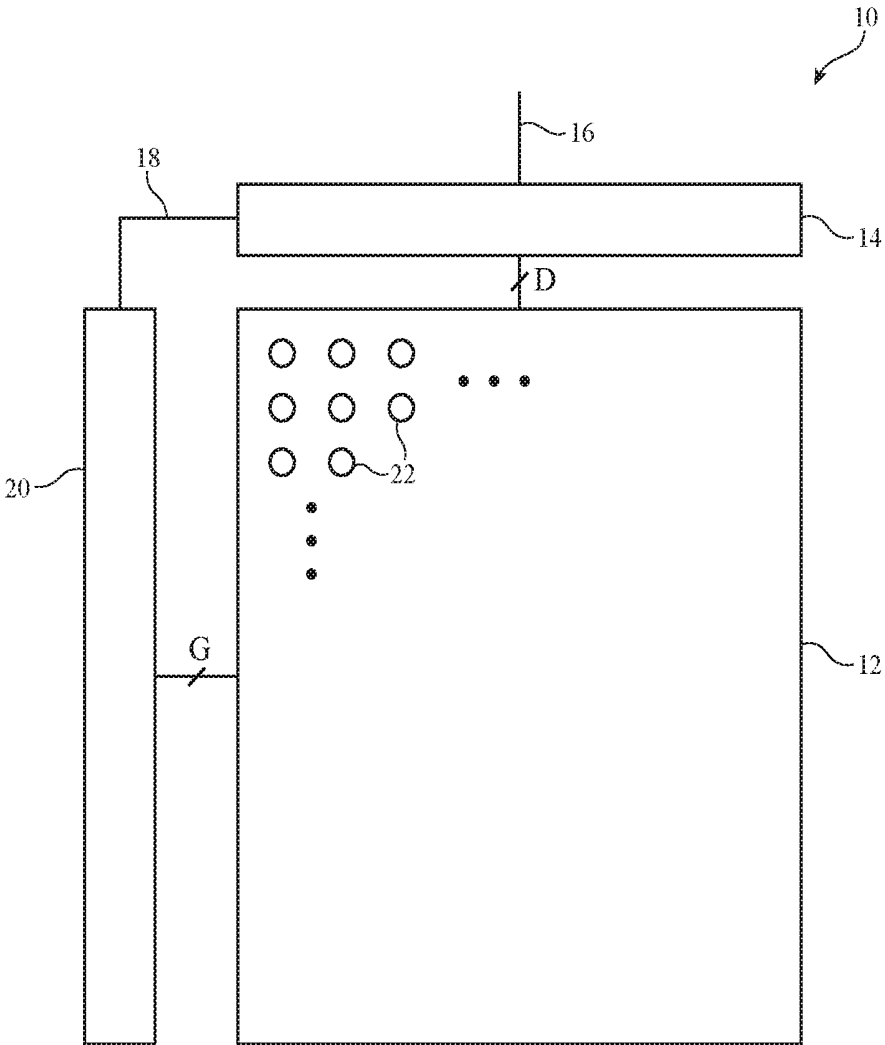


FIG. 1

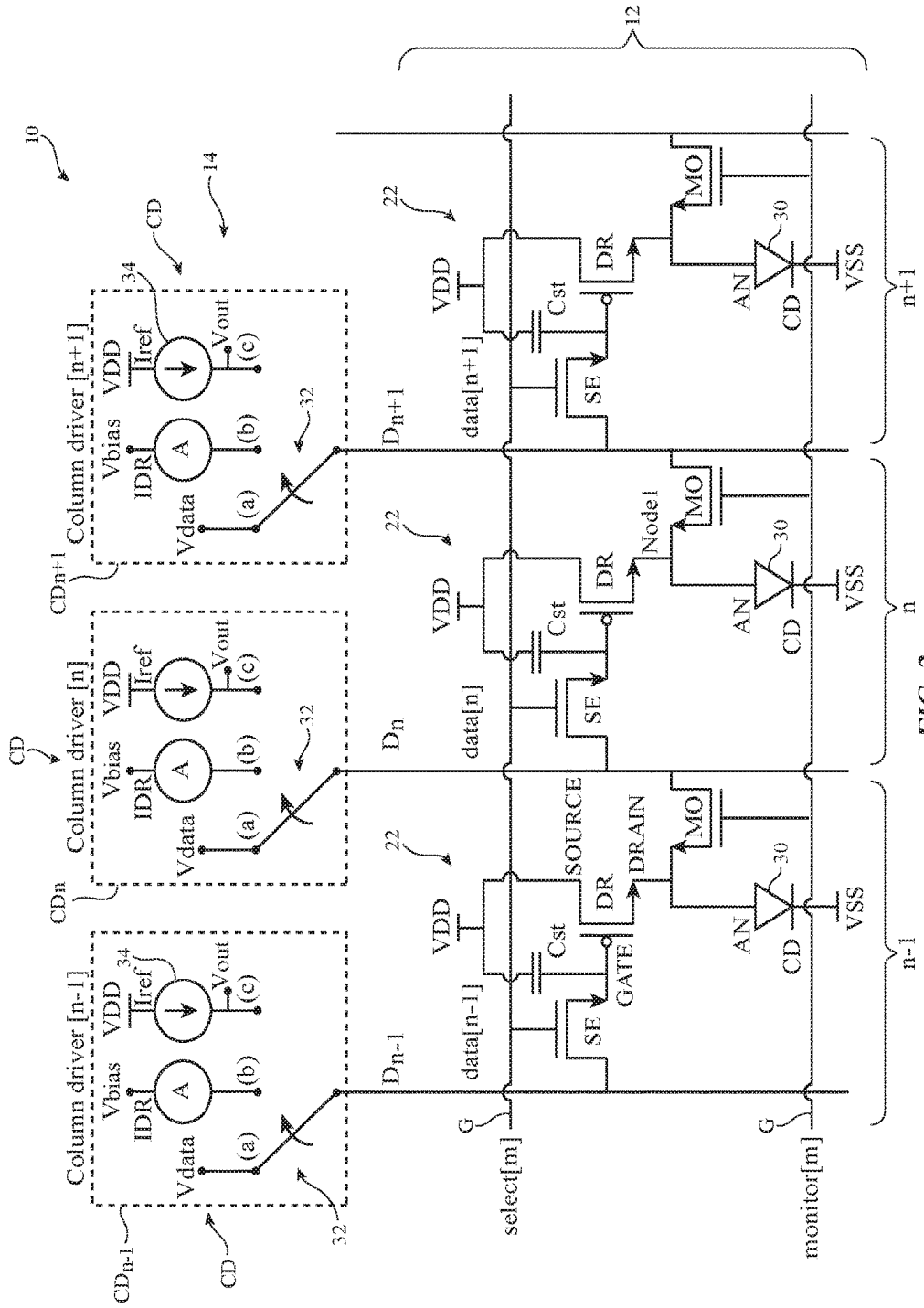


FIG. 2

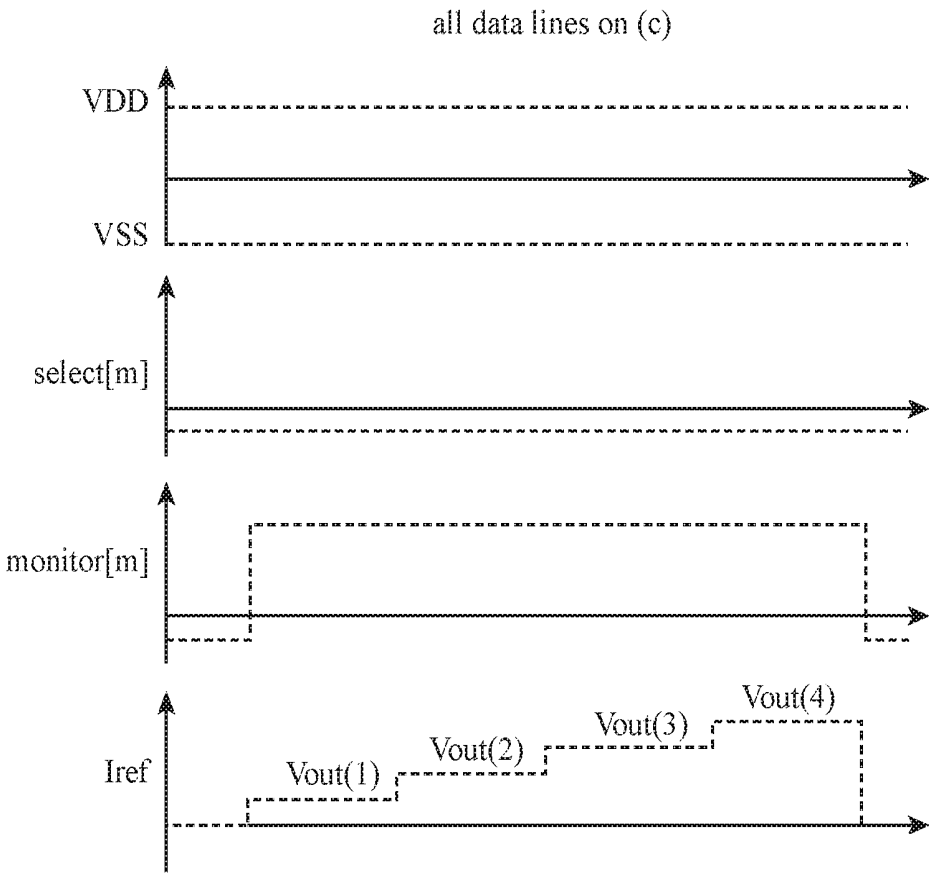


FIG. 3

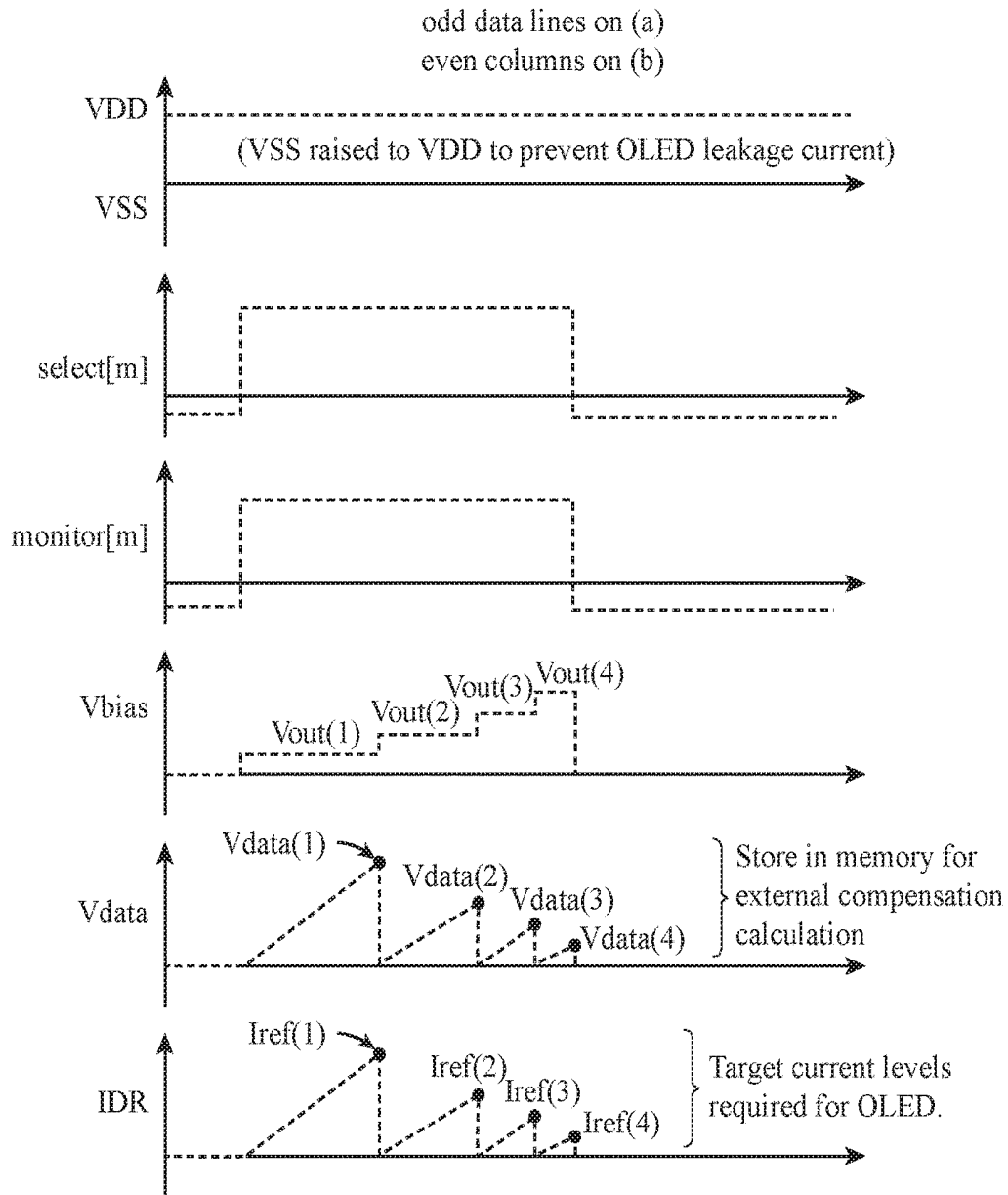


FIG. 4

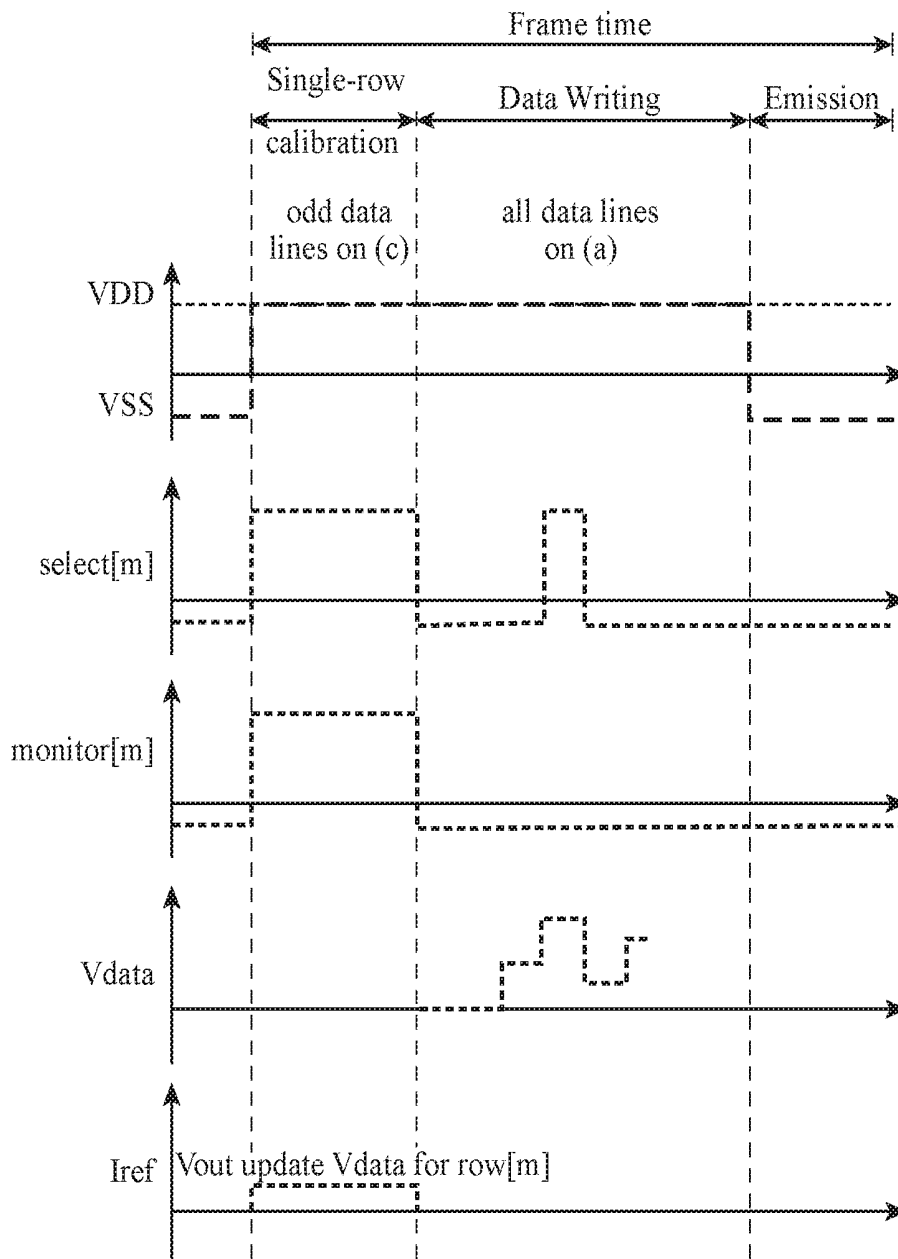


FIG. 5

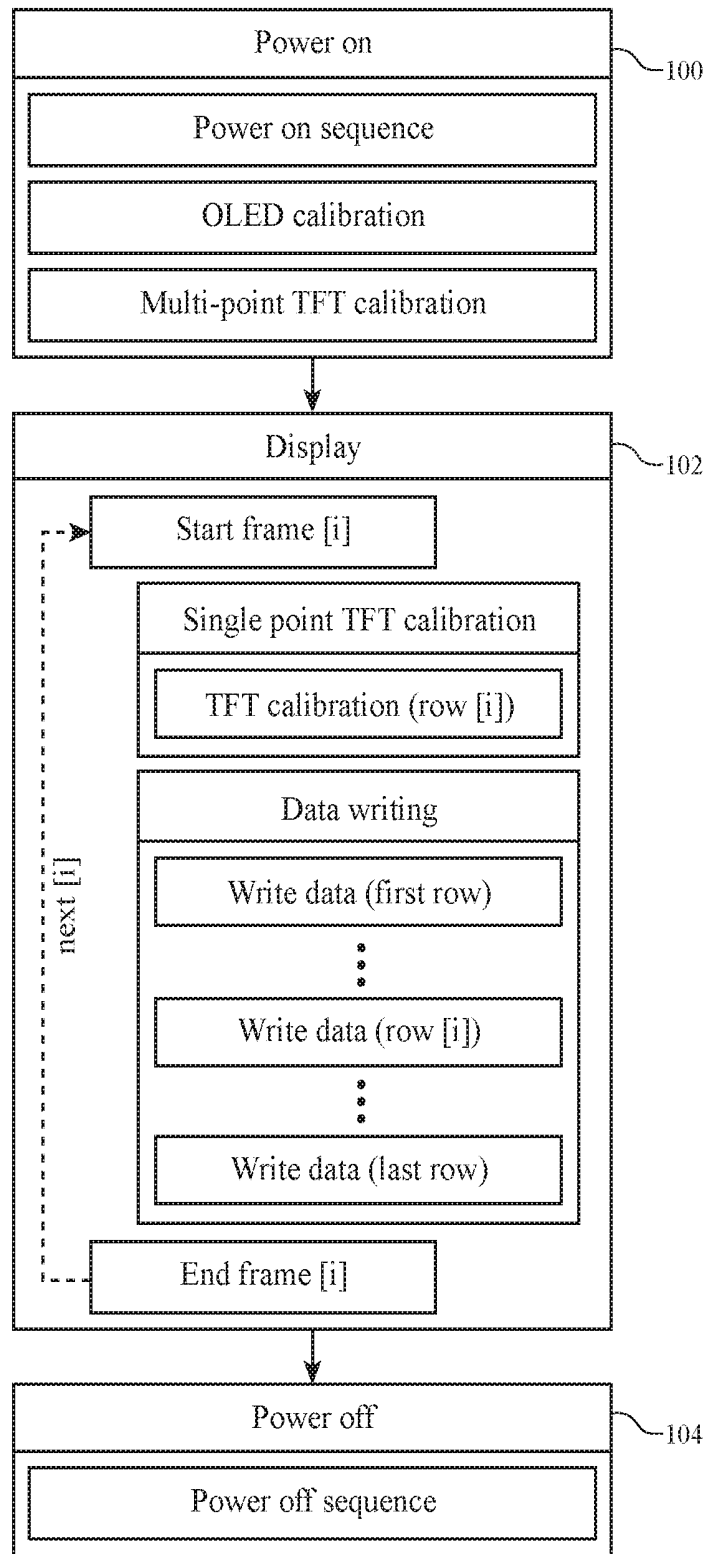


FIG. 6

ORGANIC LIGHT-EMITTING DIODE DISPLAY WITH EXTERNAL COMPENSATION

[0001] This application claims priority to provisional patent application No. 62/375,650, filed on Aug. 16, 2016, which is hereby incorporated by reference herein in its entirety.

BACKGROUND

[0002] This relates generally to displays, and, more particularly, to displays with external compensation for pixel variations.

[0003] Displays such as organic light-emitting diode displays may be used to display images for users of cellular telephones, watches, computers, and other electronic equipment. Each pixel in an organic light-emitting diode display may have an organic light-emitting diode that emits light in response to an applied current. A drive transistor and other pixel circuitry may be used to control the applied current and thereby adjust pixel brightness.

[0004] If care is not taken, pixel variations can adversely affect display performance. For example, some of the pixels in an organic light-emitting diode display may experience drive transistor threshold voltage shifts and light-emitting diode aging effects that can make those pixels weak. This can lead to undesirable variations in the image light output from different portions of a display.

[0005] Internal or external pixel compensation schemes can be used to help avoid non-uniform display output. With internal schemes, additional pixel circuitry is provided in the pixels to perform compensation. This pixel circuitry can consume significant area on a display and may therefore limit the maximum numbers of pixels per inch that can be included in the display. With external schemes, pixel measurement circuitry is placed in peripheral portions of a pixel array. These schemes allow pixel density to be enhanced, but may be insufficiently flexible to measure desired pixel attributes for compensation.

SUMMARY

[0006] A display such as an organic light-emitting diode display may have an array of pixels organized in rows and columns. There may be a data line in each column for providing image data signals to the pixels of that column. Each row may have first and second control lines coupled to the gates of first and second respective transistors in each pixel. A third transistor in each pixel may serve as a drive transistor and may be coupled in series with the light-emitting diode of that pixel between positive and ground power supply voltages.

[0007] The display may have display driver circuitry. The display driver circuitry may be used to implement an external compensation scheme. During multipoint calibration operations, the display driver circuitry may characterize each of the light-emitting diodes in a column using the data line in an adjacent column from that light-emitting diode. Each of the drive transistors in a column may be characterized using the data line in that column and the data line in an adjacent column. Multipoint calibration operations may be performed during power up and power down sequences. Single-point calibration operations in which the drive transistors are characterized to monitor for threshold voltage

shifts may be performed during normal display operation in which the display is displaying images for a user.

BRIEF DESCRIPTION OF THE DRAWINGS

[0008] FIG. 1 is a diagram of an illustrative electronic device with a display in accordance with an embodiment.

[0009] FIG. 2 is a diagram of illustrative pixels and associated display driver circuitry in accordance with an embodiment.

[0010] FIGS. 3, 4, and 5 are timing diagrams showing illustrative signals involved in performing compensation operations in accordance with an embodiment.

[0011] FIG. 6 is a flow chart of illustrative operations involved in calibrating and using a display in an electronic device in accordance with an embodiment.

DETAILED DESCRIPTION

[0012] FIG. 1 is a circuit diagram of an illustrative display. As shown in FIG. 1, display 10 may have display driver circuitry 14 that receives image data (e.g., serial image data) over path 16 from a data source (e.g., control circuitry in an electronic device associated with display 10, etc.). Images corresponding to the image data received on path 16 may be displayed on pixel array 12. Pixel array 12 may be formed from rows and columns of pixels 22. Pixels 22 may be organic light-emitting diode pixels or pixels containing micro-light-emitting diodes (e.g., individual crystalline semiconductor dies containing light-emitting diodes). Illustrative configurations in which pixels 22 are organic light-emitting diodes may sometimes be described herein as an example.

[0013] Display driver circuitry 14 may include digital-to-analog converter circuitry for converting digital image data from path 16 into analog image data signals and may include column buffer circuitry (sometimes referred to as column driver circuitry or data line driver circuitry) that drives the analog data signals into pixel array 12 for loading into pixels 22. Control signals may be supplied by display driver circuitry 14 to other display driver circuitry in display 10 such as gate line driver circuitry 20 using paths such as path 18. There may be gate line driver circuitry such as gate driver circuitry 20 on one or both edges of display 14.

[0014] During operation, display driver circuitry 14 may supply image data to pixel array 12 as data signals on data lines D while directing gate drive circuitry 20 to supply rows of pixels 22 with control signals on gate lines G. There may be any suitable number of gate lines G per row of pixels 22. For example, there may be two gate lines (control lines) G per row. There may be a single data line D per column of pixels 22. Pixels 22 may include red, green, and blue pixels and/or pixels of other colors to provide display 10 with the ability to display color images.

[0015] To compensate pixels 22 for performance variations due to manufacturing variations and/or aging effects, display 10 may include circuitry of the type shown in FIG. 2. As shown in FIG. 2, display 10 may have display driver circuitry 14 that includes a column driver circuit for each column of pixels 22 in array 12. For example, column driver CD_{n-1} may be associated with column n-1, column driver CD_n may be associated with column n, and column driver CD_{n+1} may be associated with column n+1.

[0016] Each row of pixels 22 in pixel array 12 may have a pair of associated gate lines G. Gate lines G may be used

to receive control signals from gate driver circuitry 20 (FIG. 1). In the example of FIG. 2, pixels 22 are associated with row m of array 12 and gate lines G are being used to convey control signals $\text{select}(m)$ and $\text{monitor}(m)$ to pixels 22. Control signal $\text{select}(m)$ is used to control transistors SE (sometimes referred to as “line select” transistors). Control signal $\text{monitor}(m)$ may be used to control transistors MO (sometimes referred to as “monitor” transistors).

[0017] Each pixel 22 has a respective organic light-emitting diode 30 and a drive transistor DR that controls application of drive current through that light-emitting diode. Drive transistors DR are each coupled in series with a respective light-emitting diode between positive power supply terminal V_{dd} and ground power supply terminal V_{ss} . If desired, pixels 22 may have an inverted-stacked organic light-emitting diode configuration in which anode AN of each diode 30 is coupled to positive power supply terminal V_{dd} and in which drive transistor DR is coupled between ground power supply terminal V_{ss} and the cathode CD of that diode 30 (i.e., a configuration in which drive transistor DR and diode 30 are coupled in series between positive power supply V_{dd} and ground power supply V_{ss} , but with drive transistor DR and diode 30 reversed from the orientation shown in FIG. 2). The configuration of FIG. 2 in which cathodes CD of diodes 30 are coupled to ground terminal V_{ss} and in which drive transistors DR are coupled to positive power supply terminal 22 is merely illustrative.

[0018] Pixels 22 may include storage capacitors C_{st} for storing data that is loaded into pixels 22 via data lines D . Each storage capacitor C_{st} may, as an example, have a first terminal coupled to a gate terminal of drive transistor DR and a second terminal coupled to positive power supply terminal V_{dd} . During data loading, $\text{select}(m)$ is taken high to turn on transistors SE so that data lines D may convey data signals to respective storage capacitors C_{st} through transistors SE. The magnitude of the data signal loaded onto each storage capacitor C_{st} controls the gate voltage V_g on the corresponding gate of drive transistor DR. The gate voltage V_g on the gate of drive transistor DR in a given pixel 22 controls the current flowing between the drain-source terminals of drive transistor DR and therefore the current flowing from terminal V_{dd} to terminal V_{ss} through light-emitting diode 30 and the corresponding luminance of pixel 22.

[0019] During calibration operations, the display driver circuitry of display 10 is configured to measure the behavior of the drive transistor and the behavior of the light-emitting diode in each pixel 22. This information can then be processed and used to maintain a frame of calibration data in the display driver circuitry. During normal operation, when displaying a frame of image data on display 10, the frame of calibration data may be applied to the frame of image data to calibrate the frame of image data. For example, if some of the pixels 22 in array 12 are found to have weak drive transistors and/or weak light-emitting diodes, the calibration data associated with those pixels may compensate accordingly (e.g., by increasing the value of the data loaded into those pixels by an appropriate corrective amount).

[0020] Satisfactory calibration (compensation) of the pixels 22 in array 12 may be performed by selectively configuring the transistors of each pixel 22 to couple the display driver circuitry of display 10 to each of the three terminals of drive transistors DR (to characterize drive transistors DR)

and to each of the two terminals of light-emitting diodes 30 (to characterize light-emitting diodes 30). Column driver circuitry CD and gate driver circuitry in the display driver circuitry may be dynamically configured to independently characterize transistors DR and light-emitting diodes 30, so that these measurements can be used to calibrate array 12.

[0021] As shown in FIG. 2, each column driver CD may include switching circuitry such as switch (multiplexer) 32. In each column, switch 32 may be coupled between the data line D of that column and one of three possible nodes: node (a), node (b), or node (c). Node (a) is coupled to a voltage source that produces data signals for lines D (e.g., analog image data signal V_{data}). Node (b) is coupled to a voltage source that produces a bias voltage V_{bias} via current source A . Current sensor (ammeter) A measures current I_{dr} through node (b). Node (c) is coupled to positive power supply V_{dd} via reference current source 34. Reference current source 34 supplies reference current I_{ref} . The column driver circuitry measures voltage V_{out} that is produced at node (c) in response to supplying reference current I_{ref} with current source 34.

[0022] FIG. 3 shows how the display driver circuitry of display 10 may apply signals to pixels 22 to characterize the light-emitting diodes 30 of those pixels 22. During light-emitting diode characterization operations, the current-voltage characteristic of each diode 30 may be measured. For example, diodes 30 may each be characterized at a number of different bias points (e.g., four bias points, fewer than four bias points, more than four bias points, etc.). In the example of FIG. 3, data for four different bias points is measured. Configurations in which measurements are made a multiple bias points may sometimes be referred to herein as multi-point calibration techniques.

[0023] The light-emitting diodes 30 of entire rows of pixels 22 may be characterized at the same time. As shown in FIG. 3, voltage V_{dd} may be taken high to a positive power supply voltage and voltage V_{ss} may be taken low to a ground power supply voltage. $\text{Select}(m)$ in a given row (row m) may be taken low to turn off transistors SE in that row. $\text{Monitor}(m)$ in row m may be taken high to turn on transistors MO in row m . In each column, the light-emitting diode of that column can be characterized by applying a current to that light-emitting diode through the data line of the next (adjacent) column. In particular, switch 32 in each adjacent column may be coupled to node (c) so that series of reference currents I_{ref} may be applied to the light-emitting diode 30 via the transistor MO in the column containing the light-emitting diode. As each of the reference currents I_{ref} is applied, a corresponding resulting voltage V_{out} across diode 30 may be measured at the V_{out} terminal coupled to node (c) in the adjacent column. There are four different reference currents I_{ref} and four resulting voltage $V_{out}(1)$, $V_{out}(2)$, $V_{out}(3)$, and $V_{out}(4)$ in the FIG. 3 example, but diode measurements may be taken for more bias points or fewer bias points if desired. The current and voltage measurements may be processed to characterize the current versus voltage behavior of each diode 30.

[0024] The voltage V_{out} that is measured during each of the measurements of FIG. 3 (e.g., each of the four diode bias points) corresponds to the voltage at anode AN. Anode AN is coupled to the drain terminal of drive transistor DR, so each of the V_{out} measurements of FIG. 3 may be associated with a corresponding drain voltage V_d . When drive transistor DR is controlled so as to produce a voltage V_d at its drain

that is equal to one of the measured V_{out} values, diode **30** will produce a corresponding current known current (the previously applied reference current associated with that V_{out} value). For example, if voltage $V_{out}(3)$ is measured at terminal V_{out} when current $I_{ref}(3)$ is applied to diode **30** during light-emitting diode characterization operations, then drive transistor DR can drive current $I_{ref}(3)$ through diode **30** by producing a voltage V_d at its drain that is equal to voltage $V_{out}(3)$. During drive transistor calibration operations, the magnitude of gate voltage V_g that is needed at the gate of each drive transistor to produce a given V_d value at that transistor's drain is determined. Knowing the gate voltage V_g needed to produce each drain voltage V_d (from transistor characterization operations) and knowing the amount of drain voltage V_d needed to produce a given diode current through diode **30** (from diode characterization operations), the display driver circuitry can produce a frame of calibration data containing calibration data for each of pixels **22**.

[0025] FIG. 4 shows how drive transistors DR can be characterized using measurements at multiple bias points. Half of the transistors DR in each row may be characterized at the same time. For example, the odd pixels **22** of each row in array **12** may be characterized during an initial set of characterization operations (e.g., one row at a time) and, following odd pixel characterization, the even pixels **22** of each row in array **12** may be characterized (e.g., one row at a time).

[0026] As shown in FIG. 4, positive terminals V_{dd} may be held high at a positive power supply during drive transistor characterization operations. Ground terminal V_{ss} may be temporarily raised to the same positive voltage to turn off diodes **30** and prevent leakage through diodes **30**. To characterize drive transistor DR in column n , control signal monitor(m) is taken high to turn on transistors MO in row m . In column driver CN_n of column n , switch **32** couples data line n to node (a) so that the display driver circuitry can supply various voltages V_{data} to the gate of drive transistor DR via transistor SE in column n . In adjacent column $n+1$, switch **32** of column driver CD_{n+1} is coupled to node (b). While current sensor A of column driver CD_{n+1} measures current I_{dr} flowing through data line $n+1$, bias voltage V_{bias} is taken to a series of known bias voltages. In particular, column driver CD_{n+1} supplies each of the previously measured bias point drain voltages that were gathered during the operations of FIG. 3 (i.e., voltages $V_{out}(1) \dots V_{out}(4)$) to the drain of transistor DR. At each of these applied drain voltages $V_{out}(1) \dots V_{out}(4)$, the value of V_{data} that is being applied to the gate of transistor DR in column n by column driver CD_n over data line n is adjusted (e.g., ramped as shown in FIG. 4) until the measured value of I_{dr} from current sensor A in column driver CD_n is equal to the previously measured I_{ref} value for that drain voltage (i.e., the I_{ref} values for $V_{out}(1) \dots V_{out}(4)$ that were measured during the operations of FIG. 3).

[0027] Because two data lines are used in characterizing the drive transistor DR in each column (i.e., data line n and data line $n+1$ in this example), only half of the pixels **22** in each row may be characterized at the same time. After the characterization operations are performed for the pixels in each of the odd columns, the process may be repeated for the even columns of array **12**. These measurements inform the display driver circuitry of the values of gate voltages V_g that will produce drain voltages $V_{out}(1) \dots V_{out}(4)$. The

measurements of FIG. 3 inform the display driver circuitry of the values of the light-emitting diode currents that are produced for applied drain voltages $V_{out}(1) \dots V_{out}(4)$. The multipoint measurements of FIGS. 3 and 4 therefore result in a V_g to diode current mapping that characterizes both the light-emitting diodes **30** and drive transistors DR of array **12** and allows a frame of corresponding compensation data (calibration data) to be produced and stored in display driver circuitry **14**. During normal operation of display **10**, display driver circuitry **14** can apply this calibration frame to the data of the image frames being received over path **16**, so that the corresponding image frames that are displayed on pixel array **12** by the display driver circuitry are satisfactorily compensated for variations in the performance of drive transistors DR (e.g., threshold voltage variations) and light-emitting diodes **30** (e.g., the current versus voltage characteristics of diodes **30**).

[0028] The multipoint calibration operations of FIGS. 3 and 4 may consume more time than is available within an image frame. Accordingly, multipoint operations of the types show in FIGS. 3 and 4 may be performed at times when more calibration time is available such as at power up and/or at power down. To ensure that the performance of pixels **22** does not drift excessively between power up/down events, periodic single-point transistor calibration operations can be performed. During these single-point calibration operations, threshold voltage shifts in transistors DR can be measured. Measurement time may be relatively short, so at least one of the periodic single-point transistor calibration operations can be incorporated into each frame (or nearly each frame) being displayed (e.g., in a brief time interval at the beginning of each frame, etc.).

[0029] To ensure that single-point transistor characterization measurements are sufficiently short, only a single half row (odd or even pixels) can be characterized at each time. Over hundreds or thousands of frames, the display driver circuitry can step through the pixels in all of the rows of array **12**, so that all of array **12** can be characterized. This transistor characterization information may then be used to update the calibration data in the calibration frame stored in display driver circuitry **14**.

[0030] During each single-point characterization of drive transistors DR, drive transistors DR are diode connected ($V_d=V_g$). Single-point calibration operations may occur during time period "single-row" in FIG. 5. During this time period, V_{dd} is high and V_{ss} is temporarily set high to turn off diodes **30** in row m . Select(m) and monitor(m) are taken high to turn on transistors SE and MO. In column n , column driver CD_n couples switch **32** to node (a), so V_{data} is applied to the gate of transistor DR. In column $n+1$, column driver CD_{n+1} couples switch **32** to node (c). This couples current source **34** (producing current I_{ref}) to the drain of transistor DR. The value of I_{ref} is set to a known value I_{refk} . I_{refk} is forced through transistor DR from the drain of transistor DR to the source of transistor DR. The forced current I_{refk} causes the drain voltage V_d to go to a value V_{dout} , which is measured at terminal V_{out} . Column driver CD_n applies the same voltage V_{dout} to the gate of transistor DR (as V_{data}) using data line n to place transistor DR in a diode connected configuration ($V_g=V_d$). Knowing I_{refk} and V_{dout} for diode-connected transistor DR, the display driver circuitry may compute the threshold voltage V_t of drive transistor DR. This information may then be used to update the frame of calibration data stored in display driver cir-

circuitry 12. Following calibration of the odd (or even) pixels of one of the rows of array 12, image data may be loaded (during the “data writing” phase of FIG. 5) and pixels 22 may be used to emit light (e.g., during the normal display operating mode “emission” of FIG. 5). To ensure that all transistors DR are characterized using the single-point calibration technique, the process of FIG. 5 may be repeated for each row of array 10 for both odd and even pixel sets. Diodes 30 need not be characterized during the single-point drive transistor characterization operations of FIG. 5 and can be characterized during power on and/or power off calibration operations of the type described in connection with FIGS. 3 and 4.

[0031] FIG. 6 is a flow chart of illustrative operations involved in using display 10 of FIG. 1. At step 100, display 10 may be powered on. Following a power-on sequence in which the circuitry of display 10 is powered with power supply voltages V_{dd} and V_{ss}, multipoint organic light-emitting diode characterization operations of the type shown in FIG. 3 may be performed and multipoint drive transistor characterization operations of the type shown in FIG. 4 may be performed. Calibration data (e.g., a frame of calibration data that includes compensation information for each pixel 22 of array 12) may be saved in display driver circuitry 14 and/or previous calibration data may be updated based on the multipoint characterization results.

[0032] During the operations of step 102, display 102 may be operated normally and may display image frames on pixel array 12 based on image data received over path 16. As described in connection with FIG. 5, each frame may include a short period of time (the “single-row” period from FIG. 5) during which half of a row (odd or even) of pixels 22 may be calibrated using a single-point drive transistor characterization process. Data may then be written into each of the rows of pixels 22 and a desired frame of image data displayed for a user, before looping back to characterize another half of a row (odd or even) of pixels 22. By repeatedly performing these single-point drive transistor characterization operations, threshold voltage shifts in drive transistors DR may be tracked and used to update the calibration data in display driver circuitry 14. Power-off operations may be performed during step 104. If desired, multipoint light-emitting diode and multipoint drive transistor characterization operations may be performed before performing a power off sequence that shuts down power to display 10.

[0033] In accordance with an embodiment a display is provided that includes a positive power supply terminal, a ground power supply terminal, an array of pixels having rows and columns, first and second control lines in each row, a data line associated with each column, the pixel in each row and column includes, a light-emitting diode having terminals, a drive transistor coupled in series with the light-emitting diode between the positive power supply terminal and the ground power supply terminal, a first transistor that is coupled between the data line of the column and the gate of the drive transistor and that has a gate coupled to the first control line of the row, and a second transistor that is coupled between the data line of an adjacent and one of the terminals of the diode and that has a gate coupled to the second control line of the row, and display driver circuitry configured to, characterize the light-emitting diode in each column without using any data lines in that column and using the data line in an adjacent column, and

characterize the drive transistors in each column using the data line in that column and using the data line in an adjacent column.

[0034] In accordance with another embodiment, the display driver circuitry is configured to characterize the light-emitting diode in each column using multipoint measurements.

[0035] In accordance with another embodiment, the display driver circuitry is configured to characterize the drive transistor in each column using multipoint measurements.

[0036] In accordance with another embodiment, the display includes a column driver in each column that includes a switch coupled to the data line in that column.

[0037] In accordance with another embodiment, each column driver includes a current sensor that is coupled to the switch in that column driver.

[0038] In accordance with another embodiment, the display includes a voltage bias source in each column driver that is coupled to the switch, the current sensor coupled to the switch in each column driver is coupled between the voltage bias source and the switch in that column driver.

[0039] In accordance with another embodiment, each column driver includes a current source coupled to the switch in that column driver.

[0040] In accordance with another embodiment, each column driver is configured to measure an output voltage at an output voltage sensing node coupled between the current source of that column driver and the switch of that column driver.

[0041] In accordance with another embodiment, each column driver includes a voltage source that is coupled to the switch in that column driver and that is configured to supply analog image data signals to the switch in that column driver.

[0042] In accordance with another embodiment, each pixel includes a storage capacitor and contains fewer than four transistors.

[0043] In accordance with an embodiment, a display is provided that includes an array of pixels having rows and columns, each pixel including a light-emitting diode, first, second, and third transistors, and a storage capacitor, the first transistor and light-emitting diode in each pixel are coupled in series between a positive power supply and a ground power supply, data lines associated with the array of pixels, each column contains a single respective data line, and display driver circuitry configured to characterize the light-emitting diodes in the pixels of each column using the data line in an adjacent column, characterize the first transistors in the pixels of each column using the data line in that column and the data line in an adjacent column.

[0044] In accordance with another embodiment, the display driver circuitry includes a column driver circuit in each column having a switch coupled to the data line in that column.

[0045] In accordance with another embodiment, the column driver circuit of each column has a current sensor coupled to the switch of that column driver circuit.

[0046] In accordance with another embodiment, the column driver circuit of each column is configured to supply a bias voltage to the switch through the current sensor and is configured to supply image data voltage signals to the switch.

[0047] In accordance with another embodiment, the column driver circuit of each column includes a current source coupled to the switch.

[0048] In accordance with an embodiment, a method of operating a display having an array of pixels and display driver circuitry is provided that includes with the display driver circuitry, performing multipoint light-emitting diode and drive transistor calibration operations on light-emitting diodes and drive transistors in the array of pixels, storing a frame of calibration data in the display driver circuitry based on the multipoint light-emitting diode and drive transistor calibration operations, and during normal operations in which images are being displayed on the array of pixels by the display driver circuitry, using the display driver circuitry to perform single-point calibration operations on the drive transistors to update the stored frame of calibration data.

[0049] In accordance with another embodiment, the array has rows and columns of the pixels, each of the pixels includes a respective one of the light-emitting diodes and a respective one of the drive transistors, each column includes a single data line, and performing the multipoint light-emitting diode and drive transistor calibration operations includes with the display driver circuitry, characterizing the light-emitting diode in each column using the data line in an adjacent column and no other data lines.

[0050] In accordance with another embodiment, performing the multipoint light-emitting diode and drive transistor calibration operations includes with the display driver circuitry, characterizing the thin-film transistor in each column using the data line in that column and using the data line in an adjacent column.

[0051] In accordance with another embodiment, characterizing the thin-film transistor in each column includes applying voltages to a gate of the drive transistor using the data line in that column and applying voltages to a drain of the drive transistor using the data line in the adjacent column.

[0052] In accordance with another embodiment, characterizing the light-emitting diode in each column includes applying a current to that light-emitting diode while measuring a resulting voltage using the data line in the adjacent column.

[0053] The foregoing is merely illustrative and various modifications can be made to the described embodiments. The foregoing embodiments may be implemented individually or in any combination.

What is claimed is:

1. A display, comprising:

- a positive power supply terminal;
- a ground power supply terminal;
- an array of pixels having rows and columns;
- first and second control lines in each row;
- a data line associated with each column, wherein the pixel in each row and column includes:
 - a light-emitting diode having terminals;
 - a drive transistor coupled in series with the light-emitting diode between the positive power supply terminal and the ground power supply terminal;
 - a first transistor that is coupled between the data line of the column and the gate of the drive transistor and that has a gate coupled to the first control line of the row; and

- a second transistor that is coupled between the data line of an adjacent and one of the terminals of the diode and that has a gate coupled to the second control line of the row; and

display driver circuitry configured to:

- characterize the light-emitting diode in each column without using any data lines in that column and using the data line in an adjacent column; and
- characterize the drive transistors in each column using the data line in that column and using the data line in an adjacent column.

2. The display defined in claim 1 wherein the display driver circuitry is configured to characterize the light-emitting diode in each column using multipoint measurements.

3. The display defined in claim 2 wherein the display driver circuitry is configured to characterize the drive transistor in each column using multipoint measurements.

4. The display defined in claim 1 further comprising a column driver in each column that includes a switch coupled to the data line in that column.

5. The display defined in claim 4 wherein each column driver includes a current sensor that is coupled to the switch in that column driver.

6. The display defined in claim 5 further comprising a voltage bias source in each column driver that is coupled to the switch, wherein the current sensor coupled to the switch in each column driver is coupled between the voltage bias source and the switch in that column driver.

7. The display defined in claim 6 wherein each column driver includes a current source coupled to the switch in that column driver.

8. The display defined in claim 7 wherein each column driver is configured to measure an output voltage at an output voltage sensing node coupled between the current source of that column driver and the switch of that column driver.

9. The display defined in claim 8 wherein each column driver includes a voltage source that is coupled to the switch in that column driver and that is configured to supply analog image data signals to the switch in that column driver.

10. The display defined in claim 1 wherein each pixel includes a storage capacitor and contains fewer than four transistors.

11. A display comprising:

- an array of pixels having rows and columns, each pixel including a light-emitting diode, first, second, and third transistors, and a storage capacitor, wherein the first transistor and light-emitting diode in each pixel are coupled in series between a positive power supply and a ground power supply;

data lines associated with the array of pixels, wherein each column contains a single respective data line; and display driver circuitry configured to:

- characterize the light-emitting diodes in the pixels of each column using the data line in an adjacent column;
- characterize the first transistors in the pixels of each column using the data line in that column and the data line in an adjacent column.

12. The display defined in claim 11 wherein the display driver circuitry comprises a column driver circuit in each column having a switch coupled to the data line in that column.

13. The display defined in claim **12** wherein the column driver circuit of each column has a current sensor coupled to the switch of that column driver circuit.

14. The display defined in claim **13** wherein the column driver circuit of each column is configured to supply a bias voltage to the switch through the current sensor and is configured to supply image data voltage signals to the switch.

15. The display defined in claim **14** wherein the column driver circuit of each column includes a current source coupled to the switch.

16. A method of operating a display having an array of pixels and display driver circuitry, comprising:

with the display driver circuitry, performing multipoint light-emitting diode and drive transistor calibration operations on light-emitting diodes and drive transistors in the array of pixels;

storing a frame of calibration data in the display driver circuitry based on the multipoint light-emitting diode and drive transistor calibration operations; and

during normal operations in which images are being displayed on the array of pixels by the display driver circuitry, using the display driver circuitry to perform single-point calibration operations on the drive transistors to update the stored frame of calibration data.

17. The method defined in claim **16** wherein the array has rows and columns of the pixels, wherein each of the pixels

includes a respective one of the light-emitting diodes and a respective one of the drive transistors, wherein each column includes a single data line, and wherein performing the multipoint light-emitting diode and drive transistor calibration operations comprises:

with the display driver circuitry, characterizing the light-emitting diode in each column using the data line in an adjacent column and no other data lines.

18. The method defined in claim **17** wherein performing the multipoint light-emitting diode and drive transistor calibration operations comprises:

with the display driver circuitry, characterizing the thin-film transistor in each column using the data line in that column and using the data line in an adjacent column.

19. The method defined in claim **18** wherein characterizing the thin-film transistor in each column comprises applying voltages to a gate of the drive transistor using the data line in that column and applying voltages to a drain of the drive transistor using the data line in the adjacent column.

20. The method defined in claim **18** wherein characterizing the light-emitting diode in each column comprises applying a current to that light-emitting diode while measuring a resulting voltage using the data line in the adjacent column.

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专利名称(译)	具有外部补偿的有机发光二极管显示器		
公开(公告)号	US20190206321A1	公开(公告)日	2019-07-04
申请号	US16/325236	申请日	2017-08-10
[标]申请(专利权)人(译)	苹果公司		
申请(专利权)人(译)	苹果公司.		
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IPC分类号	G09G3/3233 G09G3/3291 H01L27/32		
CPC分类号	G09G3/3233 G09G3/3291 H01L27/3276 G09G2300/0819 G09G2300/0426 G09G2300/0439 G09G2320/0693 G09G2300/0866 G09G2310/06 G09G2320/0233 G09G2320/0285 G09G2320/0295 G09G2320/045 G09G2330/026		
优先权	62/375650 2016-08-16 US		
外部链接	Espacenet USPTO		

摘要(译)

显示器具有行和列像素 (22)。每列中的数据线 (D_n) 将图像数据信号提供给该列的像素。每行具有耦合到每个像素中的第一和第二相应晶体管 (SE , MO) 的栅极的第一和第二控制线 (选择[m] , 监视器[m])。每个像素中的第三晶体管用作驱动晶体管 (DR) 并且在正和地电源电压 (VDD , VSS) 之间与发光二极管 (30) 串联耦合。显示器中的显示驱动器电路使用来自该发光二极管的相邻列中的数据线来表征列中的每个发光二极管。使用该列中的数据线和相邻列中的数据线来表征列中的每个驱动晶体管。

